## **Amendments to the Specification**

Please replace the paragraph spanning pg. 11, line 23 to page 12, line 2 with the following amended paragraph:

The process 400 is divided into [[to]] <u>two</u> phases: a preprocess phase and a runtime phase. The preprocess phase is performed at the CPU 202 in preparation [[to]] <u>for</u> the runtime phase. The blocks in Fig. 4 are separated by a dashed line to differentiate between preprocess operations and runtime operations.